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CV193DPAG

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DESCRIPTION

PC MAIN CLOCK

Compliant with Intel CK505 Gen II spec
 One high precision PLL for CPU, SSC and N programming
 One high precision PLL for SRC, SSC and N programming
 One high precision PLL for SATA/PCI, and SSC
 One high precision PLL for 96MHz/48MHz
 Push-pull IOs for differential outputs
 Support spread spectrum modulation, -0.5 down spread and others
 Support SMBus block read/write, byte read/write
 Available in TSSOP package

Generic Part:

CV193

Category:

Desktop

PARAMETERS

Package	TSSOP 64
Speed	NA
Temperature	C
Voltage	3.3 V
CPU Manufacturer	Intel®
Chipset	910
Yellow Cover Spec.	CK505 1.0
Embedded	
Suggested - Pkg. Voltage	
Core Supply Voltage (VDD)	3.3 V
Input Style	XTAL
Output Style	TYPE- SR
Output Supply Voltage (VDDO)	3.3
Temp. Grade	C

PACKAGE

Package	TSSOP 64 (PAG64)
Description	TSSOP 6.1 MM 0.5MM PITCH
Status	Active
Class	PLASTIC
Category	Green
Pb (Lead) Free	Yes
Pb Free Category	e3 Sn
Mark	PAG
Package Type	TSSOP
Lead Count	64
Length	17.0 mm
Width	6.1 mm
Thickness	1.0 mm
Pitch	0.5 mm
Tube / Tray	TUBE
Quantity per Tube/Tray	28
Quantity per Reel	2000
Moisture Sensitivity Level (MSL)	1
Moisture Exposure Floor Life	Unlimited@ <30C/85% RH
Peak Reflow Temperature	260°C
Rebake Conditions	NA
Related Package Documents	view documents

DOCUMENTS

Type	Title	Size	Revision Date
Datasheet	IDT 9CPS4592 datasheet	618 KB	04/23/2010
	CV193 Datasheet	176 KB	05/18/2009
Flyer	IDT Design Tip: What Makes A Good PC Clock?	213 KB	03/23/2010
Product Change Notice	PCN# W1002-01(R4)	378 KB	10/05/2010
	PCN# : K1008-01 Top Mark Format Change on Selective IDT Part	383 KB	08/19/2010
	PCN# : TB0902-01 TRACEABILITY MARK ON TOP OF PACKAGE	1024 KB	03/03/2009

DISTRIBUTOR INVENTORY

Part	Distributor	Qty	Date	RFQ	Order
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No matches found.

CONTACTS**Sales Offices**

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Technical Support

- [Clock Help](#)
- ▶ [WWW: Clock Help](#)

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