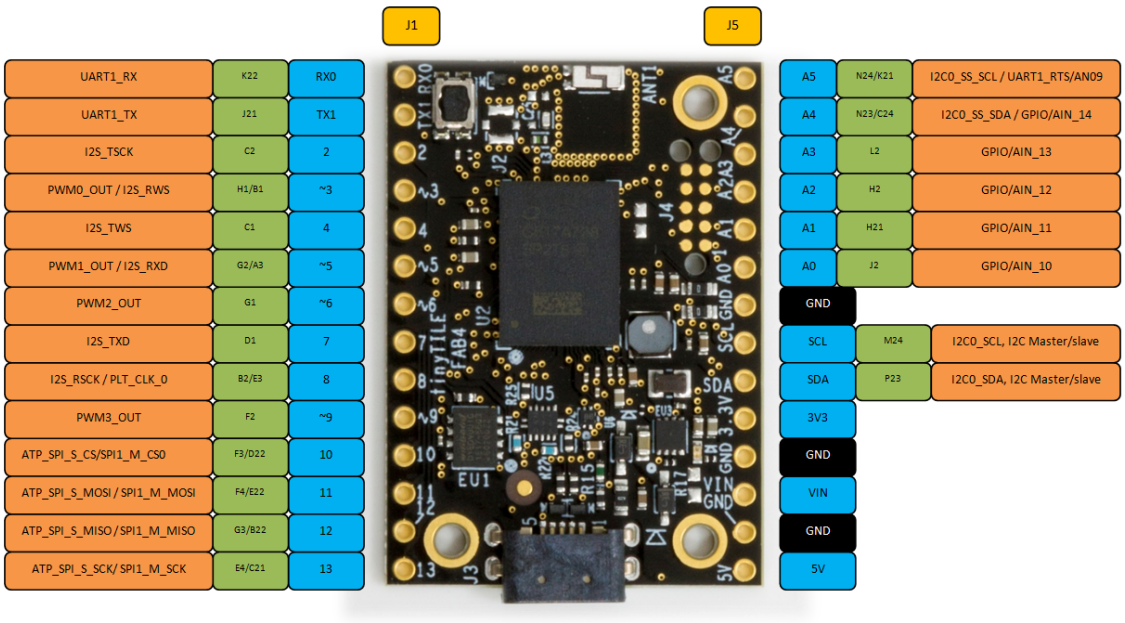
tinyTILE

Pin-mapping

intel inside™

tinyTILE has been designed in collaboration with Intel



⚠ WARNING! Applying voltages >3v3 to any I/O pins may cause irreversible damage to the circuit and/or the Curie™ module.

J1	Board Signal
RX<-0	RXD1/SS9_3V_IO0
TX->1	TXD1/SS8_3V_IO1
2	I2S_TSCK/IO18_3V_IO2
~3	PWM0/SS10_3V_IO3
4	I2S_TWS/IO19_3V_IO4
~5	PWM1/SS11_3V_IO5
~6	PWM2/SS12_3V_IO6
7	I2S_TXD/IO20_3V_IO7
8	MISO1/IO9_3V_IO8
~9	PWM3/SS13_3V_IO9
10	ATPSS/IO0_3V_IO10
11	ATPMOSI/IO3_3V_IO11
12	ATPMISO/IO1_3V_IO12
13	ATPSCK/IO2_3V_IO13

J5	Board Signal		
A5	AD09/SS1_3V_AD5	SCL0_SS_3V_AD5	I2C Master Connection
A4	AD14/SS6_3V_AD4	SDA0_SS_3V_AD4	
A3	AD13/SS5_3V_AD3		
A2	AD12/SS4_3V_AD2		
A1	AD11/SS3_3V_AD1		
A0	AD10/SS2_3V_AD0		
GND	GND		
I2C0_SCL	I2C0_SCL		I2C Slave Connection
I2C0_SDA	I2C0_SDA		
3.3V	VDD_PLAT_3P3		
GND	GND		
Vin	VIN_CONN		
GND	GND		
5V USB	USB_5P00_PWR		

	GND
	PORT PIN
	BOARD SIGNAL
	CURIE MODULE -PHYSICAL BUMP MAP
	CURIE MODULE PIN FUNCTION
	POWER

For more information visit www.element14.com/tinyTILE

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