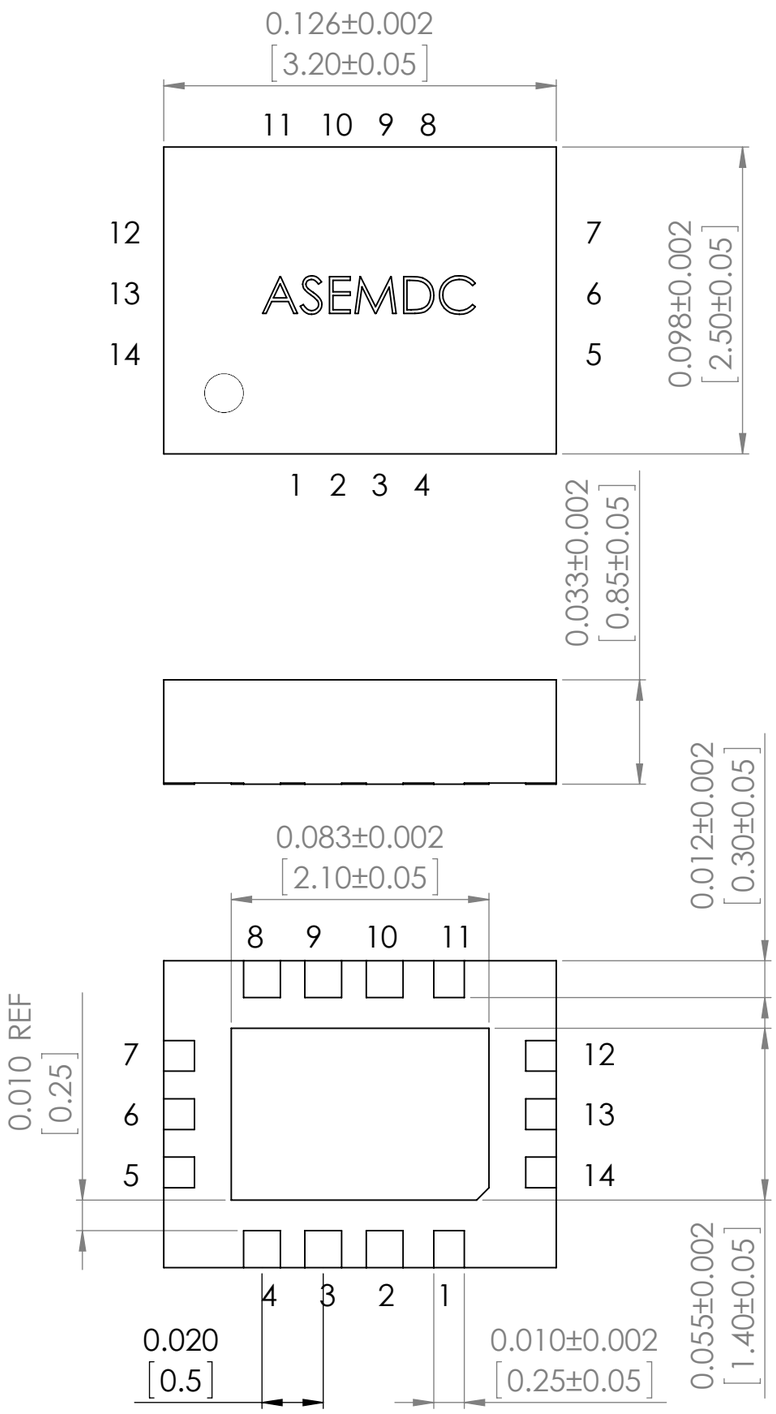
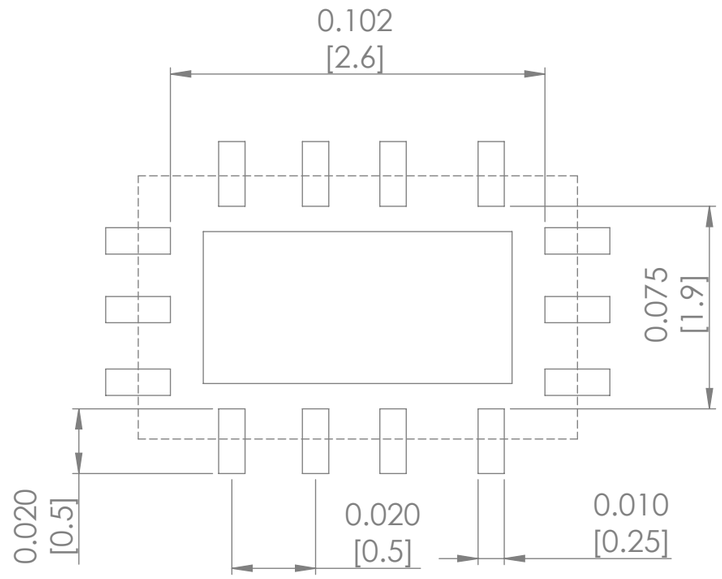
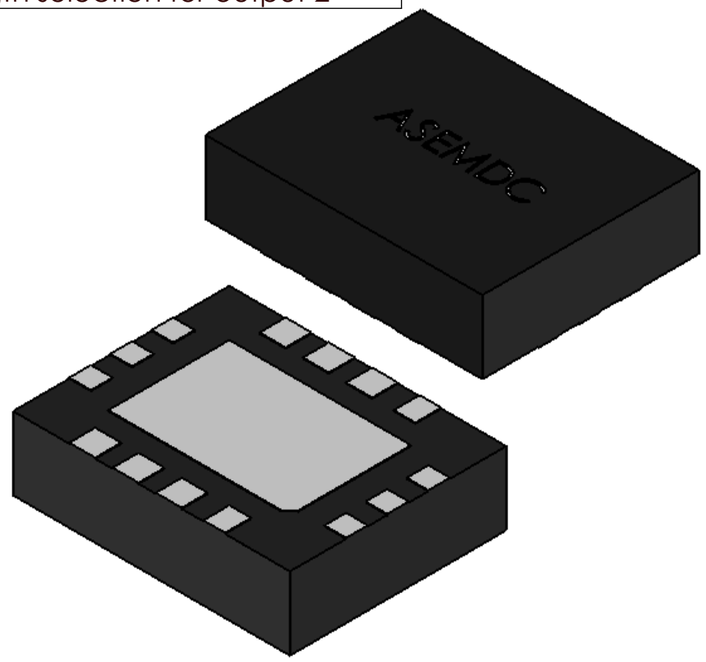


Pin	Pin Name	Pin Type	Function
1	Enable	I	Enables outputs when high and disables(tri-state) them when low
2	NC	NA	Leave unconnected or grounded
3	O2S0	I	Least significant bit for output drive strength selection for Output 2
4	GND	Power	Ground
5	FS0	I	Least significant bit for frequency selection
6	FS1	I	Middle bitfor frequency selection
7	FS2	I	Most significant bitfor frequency selection
8	Fout1	O	CMOS output 1
9	O1S0	I	Least significant bit for output drive strength selection for output 1
10	O1S1	I	Most significant bitfor output drive strength selection for output 1
11	Fout2	O	CMOS output 2
12	VDD2	Power	Power Supply for Output 2
13	VDD	Power	Power Supply
14	O2S1	I	Most significant bitfor output drive strength selection for output 2



Recommended Land Pattern



Center pad: NC/GND

Note: It is recommendedto use an approximately 0.01uF bypass capacitor between PIN 2 and 4.

TOP PACKAGE MARKING IS FOR ILLUSTRATION PURPOSES ONLY

UNLESS OTHERWISE SPECIFIED:
DIMENSIONS ARE IN MM
SURFACE FINISH:
TOLERANCES:
LINEAR:
ANGULAR:

FINISH:

DEBUR AND BREAK SHARP EDGES

DO NOT SCALE DRAWING

REVISION -

	NAME	SIGNATURE	DATE
DRAWN	SAAZVAT		
CHK'D	XXXXXX		
APPV'D			
MFG			
Q.A			

MATERIAL:

WEIGHT:

ABRACON CORPORATION
30332 Esperanza, Rancho Santa margarita, California 92688

TITLE: Crystal

DWG NO. ASEMDC

A3

SCALE:4:1

SHEET 1 OF 1