

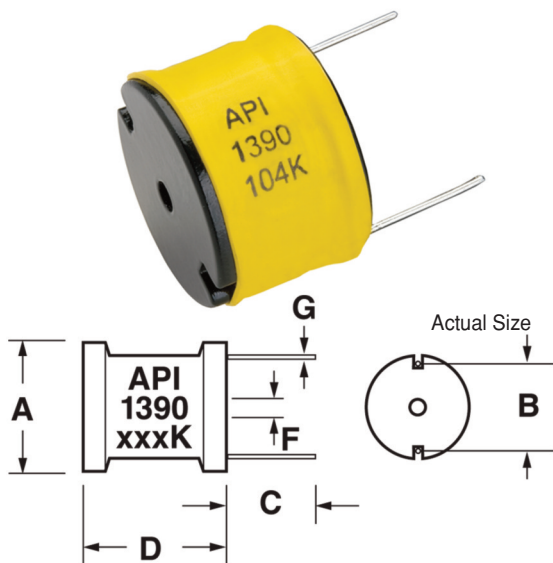
**SERIES**

**DC1390R**  
**DC1390**



**High Current Power Line Chokes**

DASH NUMBER  
INDUCTANCE (μH) ±10% @ 1.00 KHZ  
MAXIMUM (OHMS) @ 25°C  
DC RESISTANCE @ 25°C  
CURRENT RATING MAXIMUM (A DC)  
INCREMENTAL CURRENT (A DC)  
LEAD DIAMETER (Inches, Reference)



**Physical Parameters**

	Inches	Millimeters
A	1.400 to 1.480	35.56 to 37.60
B	1.150 Reference	29.21 Reference
C	0.750 Minimum	19.05 Minimum
D	1.064 Maximum	27.03 Maximum
F	0.200 Reference	5.08 Reference
G	See Table	

SERIES DC1390 and DC1390R					
-153K	15.0	0.006	19.9	33.3	0.081
-183K	18.0	0.008	17.3	30.4	0.081
-223K	22.0	0.009	16.3	28.0	0.081
-273K	27.0	0.010	15.4	25.9	0.081
-333K	33.0	0.011	14.7	22.6	0.081
-393K	39.0	0.012	14.1	21.2	0.081
-473K	47.0	0.018	11.5	20.0	0.072
-563K	56.0	0.019	11.2	17.5	0.072
-683K	68.0	0.021	10.7	16.3	0.064
-823K	82.0	0.023	10.2	14.9	0.064
-104K	100	0.025	9.76	13.2	0.064
-124K	120	0.028	9.23	12.3	0.064
-154K	150	0.040	7.72	10.8	0.057
-184K	180	0.045	7.28	9.86	0.057
-224K	220	0.050	6.90	9.09	0.057
-274K	270	0.056	6.52	8.05	0.057
-334K	330	0.074	5.67	7.37	0.051
-394K	390	0.082	5.39	6.80	0.051
-474K	470	0.114	4.57	6.20	0.046
-564K	560	0.125	4.37	5.69	0.046
-684K	680	0.139	4.14	5.11	0.046
-824K	820	0.154	3.93	4.70	0.046
-105K	1000	0.216	3.32	4.24	0.041
-125K	1200	0.232	3.21	3.87	0.041
-155K	1500	0.324	2.71	3.45	0.036
-185K	1800	0.360	2.57	3.14	0.036
-225K	2200	0.494	2.20	2.86	0.032
-275K	2700	0.555	2.07	2.58	0.032
-335K	3300	0.773	1.76	2.33	0.029
-395K	3900	0.845	1.68	2.14	0.029

\*Complete part # must include series # PLUS the dash #

For surface finish information, refer to [www.delevanfinishes.com](http://www.delevanfinishes.com)

**Operating Temperature Range** -55°C to +125°C;  
(-55°C to +80°C @ full current)

**Current Rating at 80° Ambient** 45°C Temperature Rise

**Maximum Power Dissipation at +80°C** 3.30 Watts  
Maximum

**Inductance** Measured @ 1 kHz with 0 ADC  
on Wayne Kerr 3245A, or equivalent

**Leads** Solder coated to within 1/16" of Body

**Incremental Current** The amount of DC that decreases  
the inductance by 5% maximum, relative to the 0 ADC

**Dielectric Withstanding Voltage** 1000 Vrms Minimum

**Mechanical Configuration** Center hole allows for  
mounting

**Marking** API, 1390 or 1390R, dash number (per table)  
and inductance tolerance letter

API  
1390  
xxxK

**Packaging** Bulk only